

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN THE APPLICATION OF:

WILLIAM J. BORLAND ET. AL.

CASE NO.: EL0509USNA

APPLICATION NO.: 10/718,044

CONFIRMATION NO.: 9307

GROUP ART UNIT: 2841

EXAMINER:

FILED: November 20, 2003

FOR: METHOD OF MAKING INNERLAYER PANELS AND PRINTED WIRING  
BOARDS USING X-RAY IDENTIFICATION OF FIDUCIALSSECOND SUPPLEMENTAL  
INFORMATION DISCLOSURE STATEMENTCommissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

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Respectfully submitted,

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Attorney for Applicants  
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Enclosures



Approved for use through 07/31/2006 OMB 0651-0031

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**Complete if Known**

Application Number	10/718,044
Filing Date	November 20, 2003
First Named Inventor	William J. Borland E
Art Unit	2841
Examiner Name	
Attorney Docket Number	EL0509USNA

Sheet	1	of	2
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## U. S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>2</sup>
		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				
		EP 1 265 466 A	12-11-2002	Dai Nippon Printing Co. Ltd.		
		EP 1 307 079 A	05-02-2003	Adtec Engineering Co., Ltd.		

Exam iner Signature		Date Considered	
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Substitute for form 1449B/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(Use as many sheets as necessary)</i>				<b>Complete If Known</b>	
				Application Number	10/718044
				Filing Date	November 20, 2003
				First Named Inventor	William J. Borland Et. Al.
				Art Unit	2841
				Examiner Name	
Sheet	2	of	2	Attorney Docket Number	EL0509USNA

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		Patent Abstracts of Japan, Vol. 1998, No. 06, 30 April 1998 (1998-04-30) & JP 10 051143 A (Oki Purintetsudo Circuit KK), 20 February 1998 (1998-02-20), Abstract	<input type="checkbox"/>
		Patent Abstracts of Japan, Vol. 2000, No. 02, 29 February 2000 (2000-02-29) & JP 11 330316 A (Nec Tohoku Ltd), 30 November 1999 (1999-11-30), Abstract	<input type="checkbox"/>
		Patent Abstracts of Japan, Vol. 2000, No. 22, 9 March 2001 (2001-03-09) & JP 2001 135941 A (Matsushita Electric Works Ltd.), 18 May 2001 (2001-05-18), Abstract	<input type="checkbox"/>
		Patent Abstracts of Japan, Vol. 2000, No. 16, 8 May 2001 (2001-05-08) & JP 2001 022099 A (Adtec Engineering Co. Ltd.), 26 January 2001 (2001-01-26), Abstract	<input type="checkbox"/>
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10/718,044

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